



843.37558VX

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

#10  
TBL  
2-4-03

Applicant: M. FUNABASHI  
Application No.: 09/902,673  
Filed: July 12, 2001  
For: METHOD OF MANUFACTURING A SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Art Group: 2825  
Examiner: L. Malsawma

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2800 MAIL ROOM

NOTICE OF APPEAL FROM THE PRIMARY  
EXAMINER TO THE BOARD OF APPEALS

Commissioner for Patents  
Washington, D.C. 20231

January 29, 2003

Sir:

In accordance with the provisions of 37 CFR 1.191, Applicant hereby appeals from the final rejection as set forth in the Official Action mailed July 29, 2002, finally rejecting claims 20-32.

A Credit Card Payment form in the amount of \$320.00 is enclosed, to cover the Appeal Fee.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

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